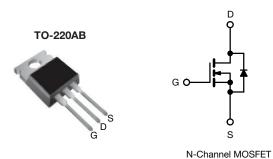
HALOGEN FREE



# **Power MOSFET**



PRODUCT SUMMARY					
V <sub>DS</sub> (V)	100				
$R_{DS(on)}(\Omega)$	V <sub>GS</sub> = 10 V 0.54				
Q <sub>g</sub> max. (nC)	8.3				
Q <sub>gs</sub> (nC)	2.3				
Q <sub>gd</sub> (nC)	3.8				
Configuration	Single				

#### **FEATURES**

- Dynamic dV/dt rating
- Repetitive avalanche rated
- 175 °C operating temperature
- · Fast switching
- · Ease of paralleling
- · Simple drive requirements
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

#### Note

\* This datasheet provides information about parts that are RoHS-compliant and / or parts that are non RoHS-compliant. For example, parts with lead (Pb) terminations are not RoHS-compliant. Please see the information / tables in this datasheet for details

## DESCRIPTION

Third generation power MOSFETs from Vishay provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness.

The TO-220AB package is universally preferred for all commercial-industrial applications at power dissipation levels to approximately 50 W. The low thermal resistance and low package cost of the TO-220AB contribute to its wide acceptance throughout the industry.

ORDERING INFORMATION				
Package	TO-220AB			
Lead (Pb)-free	IRF510PbF			
Lead (Pb)-free and halogen-free	IRF510PbF-BE3			

ABSOLUTE MAXIMUM RATINGS ( $T_C$	= 25 °C, unl	less otherwis	se noted)			
PARAMETER			SYMBOL	LIMIT	UNIT	
Drain-source voltage			$V_{DS}$	100	.,,	
Gate-source voltage			$V_{GS}$	± 20	V	
Continuous drain current	V <sub>GS</sub> at 10 V	$T_C = 25 ^{\circ}C$ $T_C = 100 ^{\circ}C$		5.6		
	V <sub>GS</sub> at 10 V	T <sub>C</sub> = 100 °C	I <sub>D</sub>	4.0	А	
Pulsed drain current <sup>a</sup>	I <sub>DM</sub>	20				
Linear derating factor				0.29	W/°C	
Single pulse avalanche energy <sup>b</sup>			E <sub>AS</sub>	75	mJ	
Repetitive avalanche current a			I <sub>AR</sub>	5.6	А	
Repetitive avalanche energy <sup>a</sup>	E <sub>AR</sub>	4.3	mJ			
Maximum power dissipation $T_C = 25  ^{\circ}C$			$P_{D}$	43	W	
Peak diode recovery dV/dt <sup>c</sup>	dV/dt	5.5	V/ns			
Operating junction and storage temperature range			T <sub>J</sub> , T <sub>stg</sub>	-55 to +175	°C	
Soldering recommendations (peak temperature) <sup>d</sup> For 10 s				300		
Mounting toyour	6-32 or M3 screw			10	lbf ⋅ in	
Mounting torque				1.1	N⋅m	

#### Notes

- a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11)
- b.  $V_{DD} = 25 \text{ V}$ , starting  $T_J = 25 \,^{\circ}\text{C}$ ,  $L = 4.8 \,\text{mH}$ ,  $R_g = 25 \,\Omega$ ,  $I_{AS} = 5.6 \,\text{A}$  (see fig. 12)
- c.  $I_{SD} \le 5.6$  A,  $dI/dt \le 75$  A/ $\mu$ s,  $V_{DD} \le V_{DS}$ ,  $T_J \le 175$  °C
- d. 1.6 mm from case

# Vishay Siliconix

THERMAL RESISTANCE RATINGS							
PARAMETER	SYMBOL	TYP.	MAX.	UNIT			
Maximum junction-to-ambient	R <sub>thJA</sub>	-	62				
Case-to-sink, flat, greased surface	R <sub>thCS</sub>	0.50	-	°C/W			
Maximum junction-to-case (drain)	R <sub>thJC</sub>	-	3.5				

<b>SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C, unless otherwise noted)							
PARAMETER	SYMBOL	TEST	MIN.	TYP.	MAX.	UNIT	
Static							
Drain-source breakdown voltage	V <sub>DS</sub>	$V_{GS} = 0$	$V_{GS} = 0 \text{ V}, I_{D} = 250 \mu\text{A}$		-	-	V
V <sub>DS</sub> temperature coefficient	$\Delta V_{DS}/T_{J}$	Reference	to 25 °C, I <sub>D</sub> = 1 mA	-	0.12	-	V/°C
Gate-source threshold voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = \	$V_{GS}$ , $I_{D} = 250 \mu\text{A}$	2.0	-	4.0	V
Gate-source leakage	I <sub>GSS</sub>	Vo	<sub>GS</sub> = ± 20 V	-	-	± 100	nA
7		V <sub>DS</sub> = 1	100 V, V <sub>GS</sub> = 0 V	-	-	25	μA
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> = 80 V, V	/ <sub>GS</sub> = 0 V, T <sub>J</sub> = 150 °C	-	-	250	
Drain-source on-state resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V	I <sub>D</sub> =3.4 A <sup>b</sup>	-	-	0.54	Ω
Forward transconductance	9 <sub>fs</sub>	$V_{DS} = 5$	50 V, I <sub>D</sub> = 3.4 A b	1.3	-	-	S
Dynamic						•	
Input capacitance	C <sub>iss</sub>	\	$V_{GS} = 0 \text{ V},$	-	180	-	
Output capacitance	C <sub>oss</sub>	V	<sub>DS</sub> = 25 V,	-	81	-	рF
Reverse transfer capacitance	C <sub>rss</sub>	f = 1.0 MHz, see fig. 5		-	15	-	
Total gate charge	$Q_{g}$		$I_D = 5.6 \text{ A}, V_{DS} = 80 \text{ V}$ $V_{DS} = 10 \text{ V},$ see fig. 6 and fig. 13 b	-	-	8.3	nC
Gate-source charge	Q <sub>gs</sub>	V <sub>GS</sub> = 10 V		-	-	2.3	
Gate-drain charge	Q <sub>gd</sub>			-	-	3.8	
Turn-on delay time	t <sub>d(on)</sub>			-	6.9	-	
Rise time	t <sub>r</sub>	$V_{DD} = 1$	$V_{DD}$ = 50 V, $I_{D}$ = 5.6 A $R_{g}$ = 24 $\Omega$ , $R_{D}$ = 8.4 $\Omega$ , see fig. 10 <sup>b</sup>		16	-	ns
Turn-off delay time	t <sub>d(off)</sub>	$R_g = 24 \Omega, R_I$			15	-	
Fall time	t <sub>f</sub>				9.4	-	
Gate input resistance	$R_{g}$	f = 1 N	MHz, open drain	2.5	-	11.6	Ω
Internal drain inductance	L <sub>D</sub>	6 mm (0.25")	Between lead, 6 mm (0.25") from		4.5	-	
Internal source inductance	L <sub>S</sub>	package and center of die contact		-	7.5	-	nH
Drain-Source Body Diode Characterist	ics	•				•	I.
Continuous source-drain diode current	Is	MOSFET symbol showing the integral reverse p - n junction diode		-	-	5.6	А
Pulsed diode forward current <sup>a</sup>	I <sub>SM</sub>			-	-	20	Α
Body diode voltage	V <sub>SD</sub>	$T_J = 25  ^{\circ}\text{C},  I_S = 5.6  \text{A},  V_{GS} = 0  \text{V}^{ \text{b}}$		-	-	2.5	V
Body diode reverse recovery time	t <sub>rr</sub>	T 25 °C 1			100	200	ns
Body diode reverse recovery charge	Q <sub>rr</sub>	$T_J = 25 ^{\circ}\text{C}, I_F = 5.6 \text{A}, dI/dt = 100 \text{A/}\mu\text{s}^{\text{b}}$		-	0.44	0.88	μC
Forward turn-on time	t <sub>on</sub>	Intrinsic tur	n-on time is negligible (tur	n-on is do	ominated	by L <sub>S</sub> and	L <sub>D</sub> )

## Notes

- a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11)
- b. Pulse width  $\leq$  300 µs; duty cycle  $\leq$  2 %



## TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

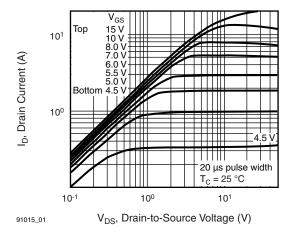


Fig. 1 - Typical Output Characteristics, T<sub>C</sub> = 25 °C

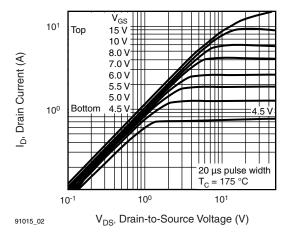


Fig. 2 - Typical Output Characteristics,  $T_C = 175$  °C

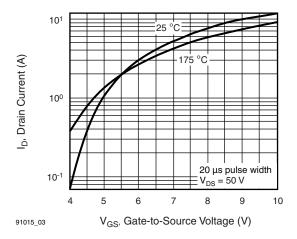


Fig. 3 - Typical Transfer Characteristics

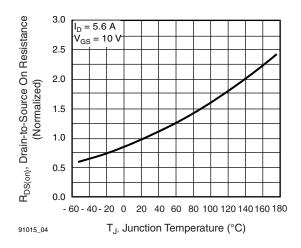


Fig. 4 - Normalized On-Resistance vs. Temperature

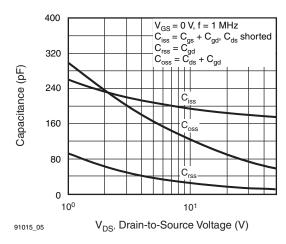


Fig. 5 - Typical Capacitance vs. Drain-to-Source Voltage

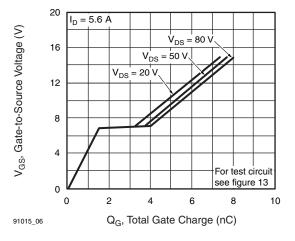


Fig. 6 - Typical Gate Charge vs. Gate-to-Source Voltage

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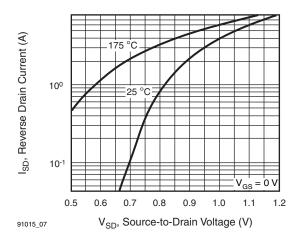


Fig. 7 - Typical Source-Drain Diode Forward Voltage

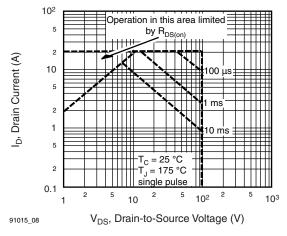


Fig. 8 - Maximum Safe Operating Area

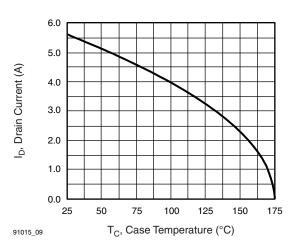


Fig. 9 - Maximum Drain Current vs. Case Temperature

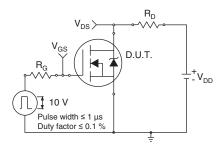


Fig. 10a - Switching Time Test Circuit

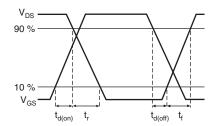


Fig. 10b - Switching Time Waveforms

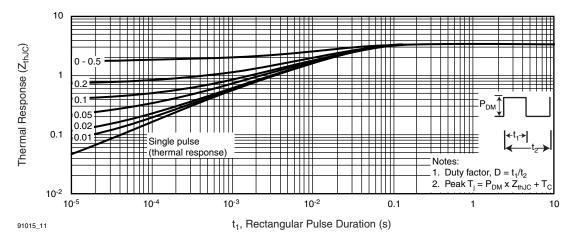


Fig. 11 - Maximum Effective Transient Thermal Impedance, Junction-to-Case



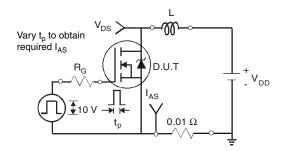


Fig. 12a - Unclamped Inductive Test Circuit

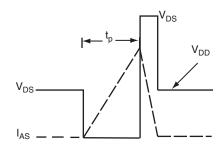


Fig. 12b - Unclamped Inductive Waveforms

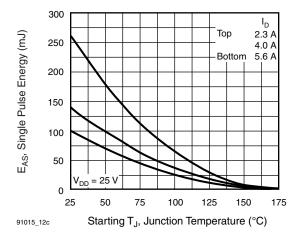


Fig. 12c - Maximum Avalanche Energy vs. Drain Current

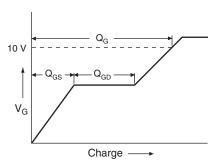


Fig. 13a - Basic Gate Charge Waveform

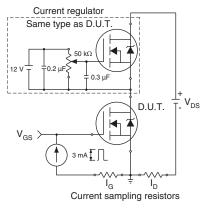
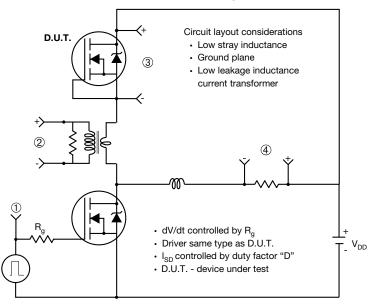


Fig. 13b - Gate Charge Test Circuit



## Peak Diode Recovery dV/dt Test Circuit



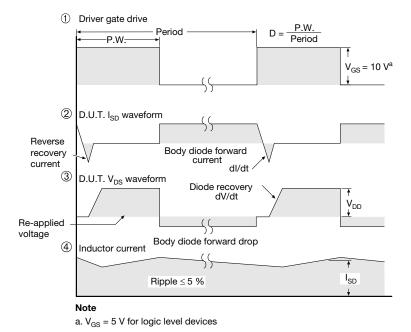


Fig. 14 - For N-Channel

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# TO-220-1



DIM	MILLIN	IETERS	INCHES			
DIM.	MIN.	MAX.	MIN.	MAX.		
Α	4.24	4.65	0.167	0.183		
b	0.69	1.02	0.027	0.040		
b(1)	1.14	1.78	0.045	0.070		
С	0.36	0.61	0.014	0.024		
D	14.33	15.85	0.564	0.624		
E	9.96	10.52	0.392	0.414		
е	2.41	2.67	0.095	0.105		
e(1)	4.88	5.28	0.192	0.208		
F	1.14	1.40	0.045	0.055		
H(1)	6.10	6.71	0.240	0.264		
J(1)	2.41	2.92	0.095	0.115		
L	13.36	14.40	0.526	0.567		
L(1)	3.33	4.04	0.131	0.159		
ØР	3.53	3.94	0.139	0.155		
Q	2.54	3.00	0.100	0.118		
ECN: X15-0364-Rev. C, 14-Dec-15 DWG: 6031						

### Note

 $\bullet$   $M^{\star}=0.052$  inches to 0.064 inches (dimension including protrusion), heatsink hole for HVM



Revison: 14-Dec-15 1 Document Number: 66542



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