100V Nch+Nch Small Signal MOSFET

V_{DSS}	100V
R _{DS(on)} (Max.)	325mΩ
I _D	±2A
P _D	1.5W

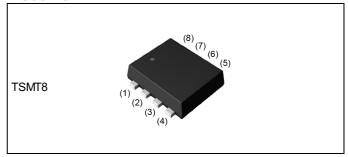
Features

- 1) Low on resistance
- 2) Small Surface Mount Package (TSMT8)

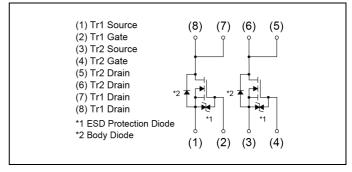
Application

Switching

Outline



Inner circuit



Packaging specifications

	Packing	Embossed Tape
	Reel size (mm)	180
Type	Tape width (mm)	8
	Basic ordering unit (pcs)	3000
	Taping code	TR
	Marking	K51

ullet Absolute maximum ratings (T_a = 25°C ,unless otherwise specified) <Tr1 and Tr2>

Parameter	Symbol	Value	Unit	
Drain - Source voltage		V _{DSS}	100	V
Continuous drain current		I _D	±2	Α
Pulsed drain current	I _{DP} *1	±6	Α	
Gate - Source voltage	V _{GSS}	±20	V	
	total	D *2	1.5	W
Power dissipation	element	P_D^{*2}	1.25	
total		P _D *3	1.1	
Junction temperature	T _j	150	°C	
Operating junction and storage temp	T _{stg}	-55 to +150	°C	

●Thermal resistance

Parameter		Cymahal	Values			Lloit
		Symbol	Min.	Тур.	Max.	Unit
	total	D *2	-	-	83.3	
Thermal resistance, junction - ambient	element	R _{thJA} *2	-	-	100	°C/W
	total	R _{thJA} *3	-	-	113	

●Electrical characteristics (T_a = 25°C) <Tr1 and Tr2>

Damanatan	Cumbal	0 1141	Values			
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Drain - Source breakdown voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 1mA	100	-	-	V
Breakdown voltage	ΔV _{(BR)DSS}	I _D = 1mA		116.9		mV/°C
temperature coefficient	ΔT _j	referenced to 25°C	-	116.9	-	mv/ C
Zero gate voltage drain current	I _{DSS}	V _{DS} = 100V, V _{GS} = 0V	-	-	1	μA
Gate - Source leakage current	I _{GSS}	$V_{DS} = 0V, V_{GS} = \pm 20V$	-	-	±10	μA
Gate threshold voltage	V _{GS(th)}	V _{DS} = 10V, I _D = 1mA		-	2.5	V
Gate threshold voltage	$\Delta V_{GS(th)}$	I _D = 1mA		2.6		m\//°C
temperature coefficient	ΔT_j	referenced to 25°C	-	-3.6	-	mV/°C
		V _{GS} = 10V, I _D = 2A	-	240	325	
Static drain - source on - state resistance	R _{DS(on)} *4	V _{GS} = 4.5V, I _D = 2A	-	250	340	mΩ
		V _{GS} = 4.0V, I _D = 2A	-	260	355	
Gate resistance	R_{G}	f = 1MHz, open drain	-	8.5	-	Ω
Forward Transfer Admittance	Y _{fs} *4	V _{DS} = 10V, I _D = 2A	1.9	-	-	S

● Electrical characteristics (T_a = 25°C) < Tr1 and Tr2>

Daramatar	Symbol	Conditions		Unit			
Parameter	Symbol Conditions —		Min.	Тур.	Max.	Offic	
Input capacitance	C _{iss}	V _{GS} = 0V	-	290	-		
Output capacitance	C _{oss}	V _{DS} = 25V	1	30	-	pF	
Reverse transfer capacitance	C _{rss}	f = 1MHz	ı	20	1		
Turn - on delay time	t _{d(on)} *4	V _{DD} ≈ 50V,V _{GS} = 10V	-	10	-		
Rise time	t _r *4	I _D = 1A	-	10	-		
Turn - off delay time	t _{d(off)} *4	$R_L = 49.9\Omega$	-	30	-	ns	
Fall time	t _f *4	$R_G = 10\Omega$	-	15	-		

● Gate charge characteristics (T_a = 25°C) < Tr1 and Tr2>

Darameter	Cymbol	Conditions	Values			Unit
Parameter	Symbol	ool Conditions		Тур.	Max.	Offic
Total gate charge	Q_g^{*4}	V _{DD} ≈ 50V	-	4.7	-	
Gate - Source charge	Q _{gs} *4	I _D = 2A	-	1.2	-	nC
Gate - Drain charge	Q _{gd} *4	V _{GS} = 5V	-	1.8	-	

● Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

<Tr1 and Tr2>

Daramatar	Cymahal	Conditions	Values			l leit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Continuous forward current	I _S	T = 25°C	-	-	1.0	^
Pulse forward current	I _{SP} *1	T _a = 25°C	-	-	6	A
Forward voltage	V _{SD} *4	V _{GS} = 0V, I _S = 2A	-	-	1.2	V

^{*1} Pw≦10µs , Duty cycle≦1%

^{*2} Mounted on a ceramic board (30×30×0.8mm)

^{*3} Mounted on a FR4 (25×25×0.8mm)

^{*4} Pulsed

Fig.1 Power Dissipation Derating Curve

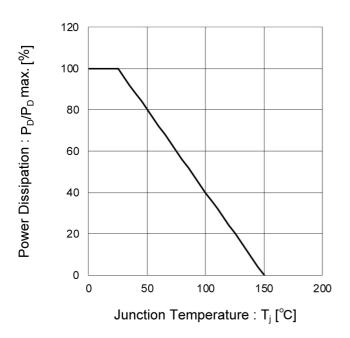
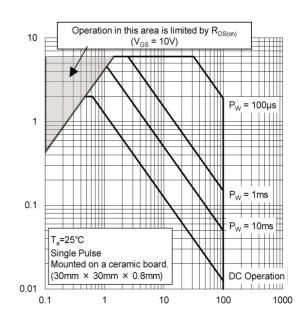


Fig.2 Maximum Safe Operating Area



Drain Current : I_D [A]

Drain - Source Voltage : V_{DS} [V]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

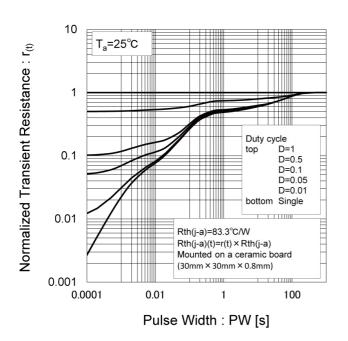
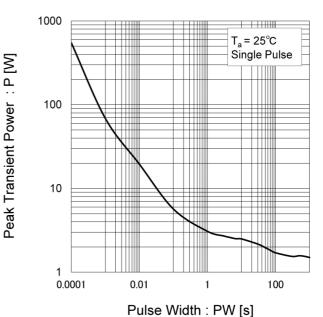


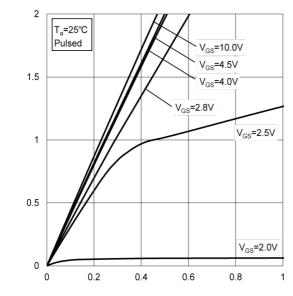
Fig.4 Single Pulse Maximum Power dissipation



Drain Current : I_D [A]

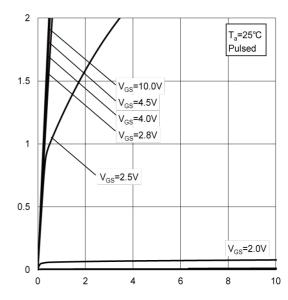
Electrical characteristic curves

Fig.5 Typical Output Characteristics(I)



Drain - Source Voltage : V_{DS} [V]

Fig.6 Typical Output Characteristics(II)



Drain Current : I_D [A]

Drain - Source Voltage : V_{DS} [V]

Fig.7 Breakdown Voltage vs.
Junction Temperature

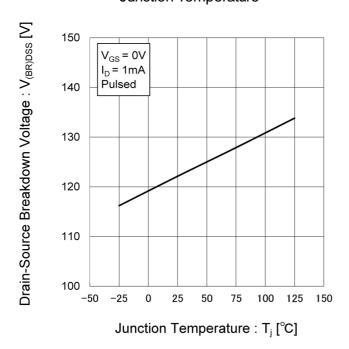
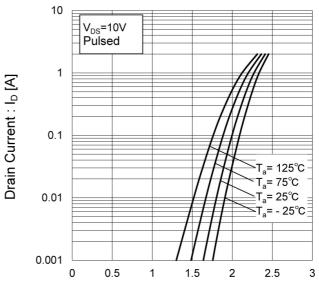


Fig.8 Typical Transfer Characteristics



Gate - Source Voltage : V_{GS} [V]

Fig.9 Gate Threshold Voltage vs.
Junction Temperature

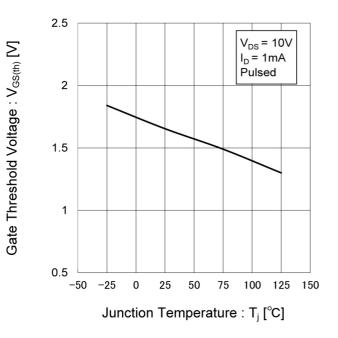
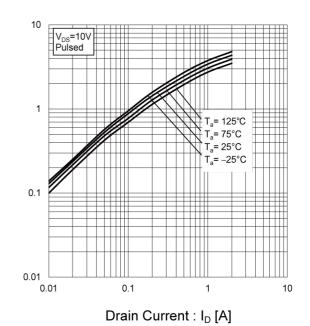


Fig.10 Forward Transfer Admittance vs.
Drain Current



Forward Transfer Admittance : $|\mathsf{Y}_{\mathsf{fs}}|$ $[\mathsf{S}]$

Fig.11 Drain Current Derating Curve

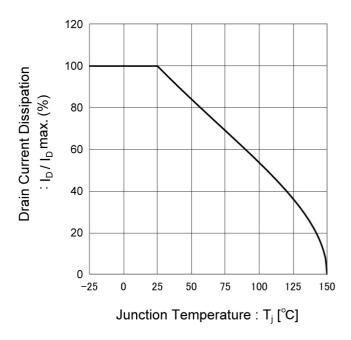
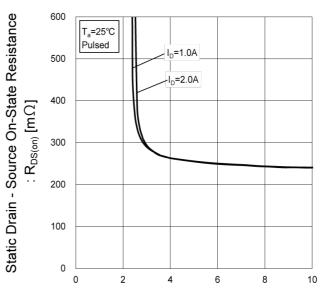


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage



Gate - Source Voltage : V_{GS} [V]

Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature

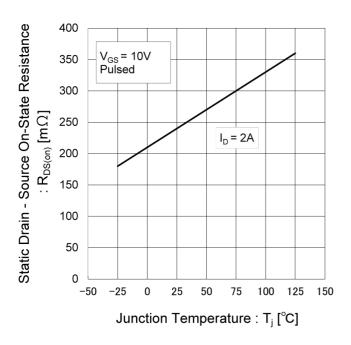


Fig.14 Static Drain - Source On - State Resistance vs. Drain Current (I)

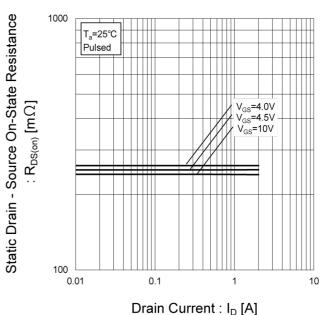


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current (II)

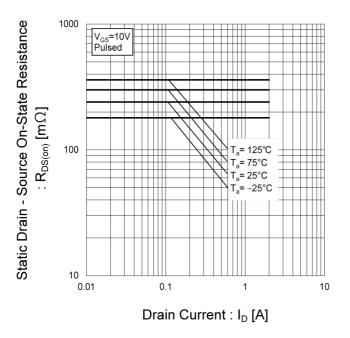


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (III)

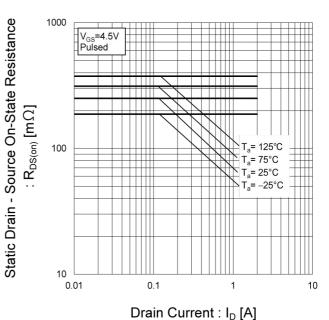


Fig.17 Static Drain - Source On - State Resistance vs. Drain Current (IV)

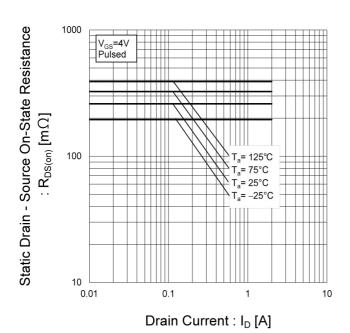
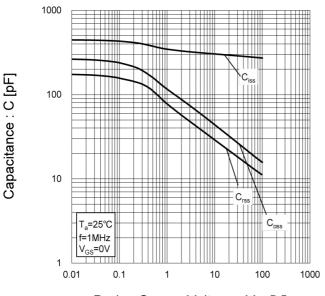


Fig.18 Typical Capacitance vs.

Drain - Source Voltage



Drain - Source Voltage : $V_{DS}[V]$

Fig.19 Switching Characteristics

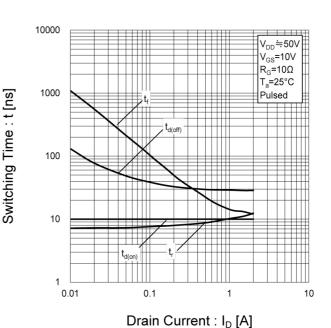
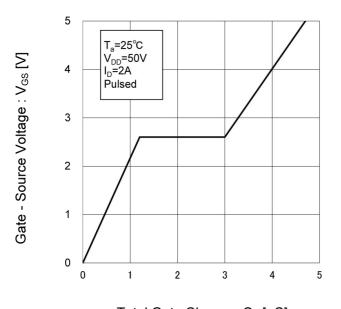


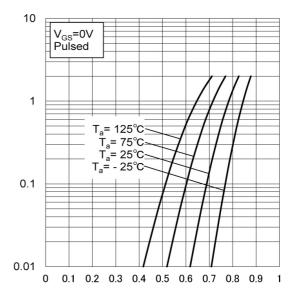
Fig.20 Dynamic Input Characteristics



Total Gate Charge : Q_g [nC]

Fig.21 Source Current vs.

Source Drain Voltage



Source-Drain Voltage: V_{SD}[V]

Source Current : Is [A]

• Measurement circuits < It is the same for the Tr1 and Tr2>

Fig.1-1 Switching Time Measurement Circuit

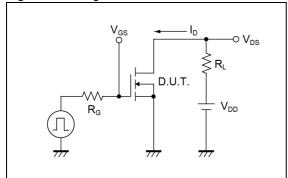


Fig.2-1 Gate Charge Measurement Circuit

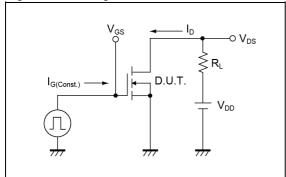


Fig.1-2 Switching Waveforms

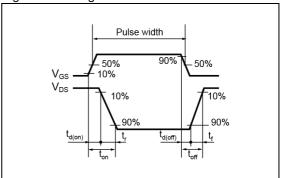
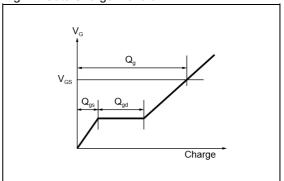


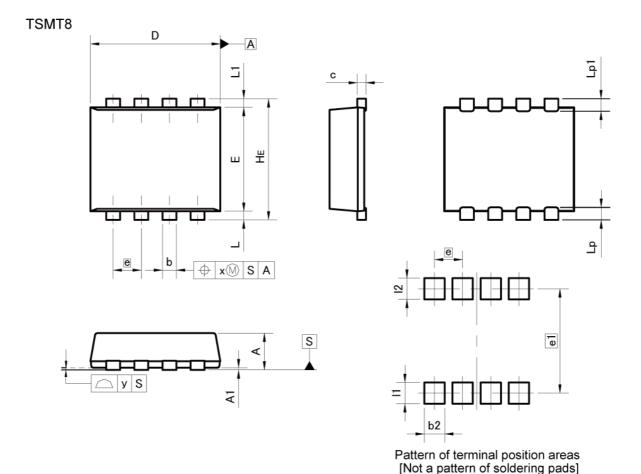
Fig.2-2 Gate Charge Waveform



Notice

This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.

Dimensions



MILIMETERS INCHES DIM MIN MAX MIN MAX Α 0.75 0.85 0.030 0.033 A1 0.00 0.05 0.000 0.002 b 0.27 0.37 0.011 0.015 С 0.12 0.22 0.005 0.009 D 2.90 3.10 0.114 0.122 2.30 2.50 0.091 0.098 Ε 0.026 0.65 е 2.70 2.90 0.106 0.114 HE 0.10 0.30 0.004 0.012 L L1 0.10 0.30 0.004 0.012 Lp 0.19 0.39 0.007 0.015 0.19 0.39 0.007 0.015 Lp1 0.10 0.004 0.10 0.004

DIM	MILIMETERS		INC	HES
DIM	MIN MAX		MIN	MAX
b2	-	0.47	-	0.019
e1	2.	2.41		95
I1	i –	0.49	-	0.019
12	1,-	0.49	-	0.019

Dimension in mm/inches



Notice

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JAPAN	USA	EU	CHINA
CLASSⅢ	CI ACCIII	CLASS II b	CI VCCIII
CLASSIV	CLASSII	CLASSⅢ	CLASSⅢ

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 - [b] Installation of redundant circuits to reduce the impact of single or multiple circuit failure
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 - [a] Use of our Products in any types of liquid, including water, oils, chemicals, and organic solvents
 - [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
 - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

Precautions Regarding Application Examples and External Circuits

- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
- You agree that application notes, reference designs, and associated data and information contained in this document are presented only as guidance for Products use. Therefore, in case you use such information, you are solely responsible for it and you must exercise your own independent verification and judgment in the use of such information contained in this document. ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of such information.

Precaution for Electrostatic

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

Precaution for Storage / Transportation

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
 - [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
 may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
 exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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A two-dimensional barcode printed on ROHM Products label is for ROHM's internal use only.

Precaution for Disposition

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