

Vishay General Semiconductor

Surface Mount Glass Passivated Rectifier



DO-214AC (SMA)

PRIMARY CHARACTERISTICS								
I _{F(AV)} 1.0 A								
V _{RRM}	50 V to 1000 V							
I _{FSM}	40 A, 30 A							
E _{AS}	5 mJ							
I _R	1.0 μΑ, 5.0 μΑ							
V _F	1.1 V							
T _j max.	150 °C							

FEATURES





- · Glass passivated chip junction
- · Low forward voltage drop
- · Low leakage current
- High forward surge capability
- Meets MSL level 1, per J-STD-020C, LF max peak of 260 °C
- Solder dip 260 °C, 40 seconds
- Component in accordance to RoHS 2002/95/EC and WEEE 2002/96/EC

TYPICAL APPLICATIONS

For use in general purpose rectification of power supplies, inverters, converters and free-wheeling diodes for consumer, automotive and telecommunication.

MECHANICAL DATA

Case: DO-214AC (SMA)

Epoxy meets UL 94V-0 flammability rating

Terminals: Matte tin plated leads, solderable per

J-STD-002B and JESD22-B102D

E3 suffix for commercial grade, HE3 suffix for high

reliability grade (AEC Q101 qualified)

Polarity: Color band denotes cathode end

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)									
PARAMETER	SYMBOL	S1A	S1B	S1D	S1G	S1J	S1K	S1M	UNIT
Device marking code		SA	SB	SD	SG	SJ	SK	SM	
Maximum recurrent peak reverse voltage	V_{RRM}	50 100 200 400 600 800				1000	٧		
Maximum RMS voltage	V _{RMS}	35	35 70 140 280 420		560	700	V		
Maximum DC blocking voltage	V_{DC}	50 100 200 400 600		800	1000	V			
Maximum average forward rectified current (see Fig.1)	I _{F(AV)}	1.0						Α	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I _{FSM}	40 30					80	Α	
Non-repetitive peak reverse avalanche energy at 25 °C, I _{AS} = 1 A, L = 10 mH	E _{AS}	5						mJ	
Operating junction and storage temperature range	T _J , T _{STG}	- 55 to + 150					°C		

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ELECTRICAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)										
PARAMETER	TEST CONDITIONS	SYMBOL	S1A S1B S1D S1G S1J S1K S1					S1M	UNIT	
Maximum instantaneous forward voltage	at 1.0 A	V _F	1.1				1.1			V
Maximum DC reverse current	T _A = 25 °C T _A = 125 °C	I _R	1.0						5.0	
at Rated DC blocking voltage			50						– μΑ	
Typical reverse recovery time	at $I_F = 0.5 \text{ A}$, $I_R = 1.0 \text{ A}$, $I_{rr} = 0.25 \text{ A}$	t _{rr}	1.8				μs			
Typical junction capacitance	at 4.0 V, 1 MHz	CJ	12					pF		

THERMAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)								
PARAMETER SYMBOL S1A S1B S1D S1G S1J S1K S1M					UNIT			
Typical thermal resistance (1)	$R_{ hetaJA} \ R_{ hetaJL}$	75 27		8		°C/W		

Note:

(1) Thermal resistance from junction to ambient and from junction to lead mounted on P.C.B. with 0.2 x 0.2" (5.0 x 5.0 mm) copper pad areas

ORDERING INFORMATION (Example)								
PREFERRED P/N	UNIT WEIGHT (g)	REFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE				
S1J-E3/61T	0.064	61T	1800	7" Diameter Plastic Tape & Reel				
S1J-E3/5AT	0.064	5AT	7500	13" Diameter Plastic Tape & Reel				
S1JHE3/61T ⁽¹⁾	0.064	61T	1800	7" Diameter Plastic Tape & Reel				
S1JHE3/5AT ⁽¹⁾	0.064	5AT	7500	13" Diameter Plastic Tape & Reel				

Note:

(1) Automotive grade AEC Q101 qualified

RATINGS AND CHARACTERISTICS CURVES

(T_A = 25 °C unless otherwise noted)

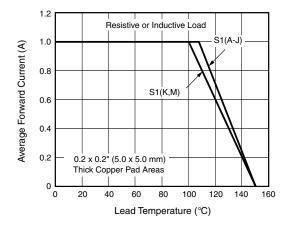


Figure 1. Forward Current Derating Curve

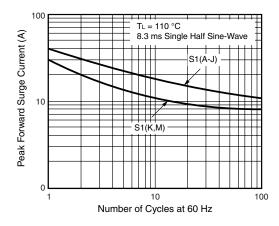


Figure 2. Maximum Non-Repetitive Peak Forward Surge Current

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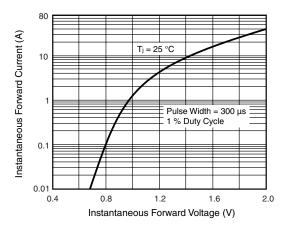


Figure 3. Typical Instantaneous Forward Characteristics

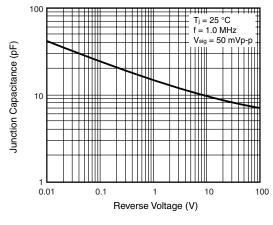


Figure 5. Typical Junction Capacitance

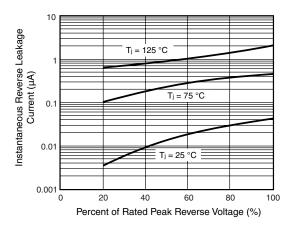


Figure 4. Typical Reverse Leakage Characteristics

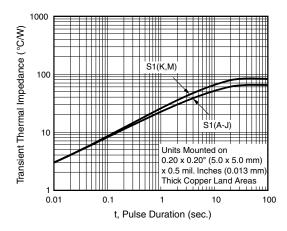
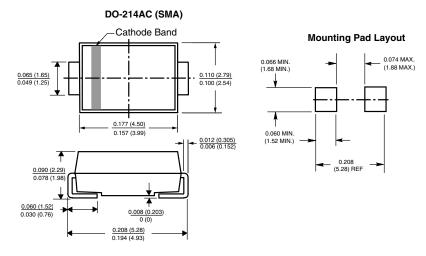


Figure 6. Typical Transient Thermal Impedance

PACKAGE OUTLINE DIMENSIONS in inches (millimeters)



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