

# ● Serise

Standard Fast Recovery

# Application

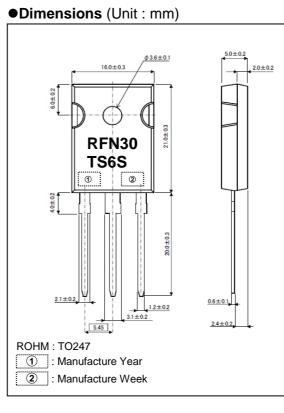
General rectification

# Features

- 1) Low switching loss
- 2) Low forward voltage
- 3) High current overload capacity

# Construction

Silicon epitaxial planar type



#### •Structure



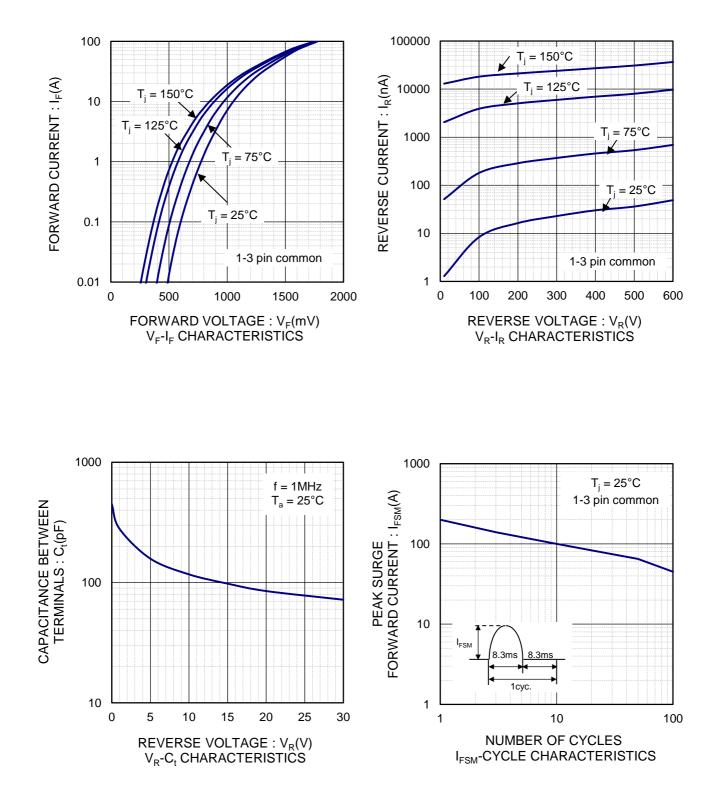
# • Absolute maximum ratings ( $T_a = 25^{\circ}C$ )

Parameter	Symbol	I Conditions Limits		Unit		
Repetitive peak reverse voltage	$V_{RM}$	Duty≦0.5	600	V		
Reverse voltage	V <sub>R</sub>	Direct voltage 600		V		
Average current	I <sub>o</sub>	60Hz half sin wave, Resistive load	30	А		
Non-repetitive forward surge current	I <sub>FSM</sub>	60Hz half sin wave, Non-repetitive at T <sub>j</sub> =25°C, 1-3 pin common		A		
Operating junction temperature	Τ <sub>j</sub>	- 150		°C		
Storage temperature	T <sub>stg</sub>	55 to +150		°C		

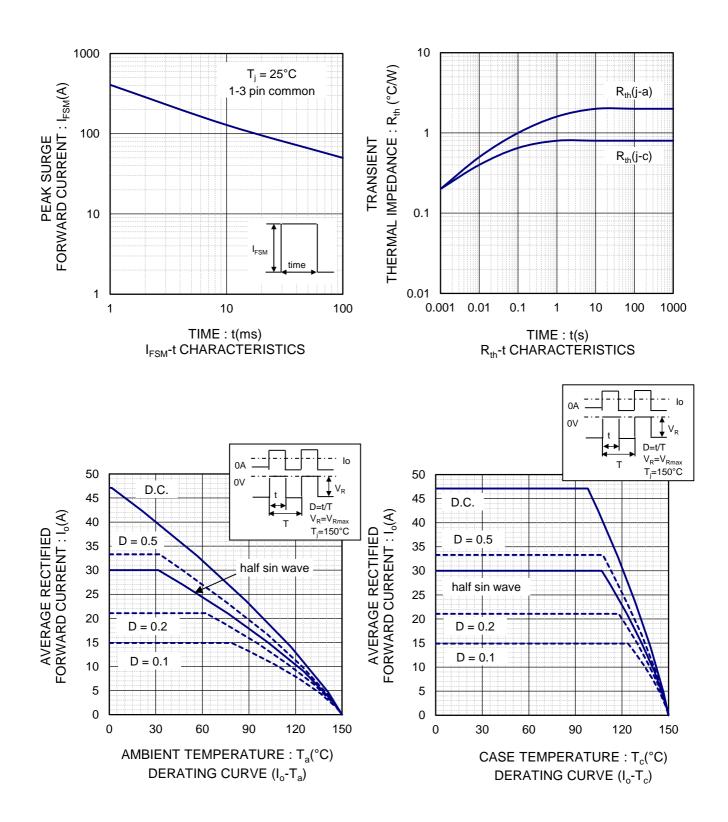
# •Electrical characteristics (T<sub>j</sub> = 25°C)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Forward voltage	$V_{F}$	I <sub>F</sub> =30A	1.0	1.3	1.55	V
Reverse current	I <sub>R</sub>	V <sub>R</sub> =600V	-	0.05	10	μA
Reverse recovery time	trr	$I_F=0.5A, I_R=1A, Irr=0.25 \times I_R$	-	36	60	ns
Thermal resistance	R <sub>th(j-c)</sub>	Junction to case	-	-	0.8	°C / W

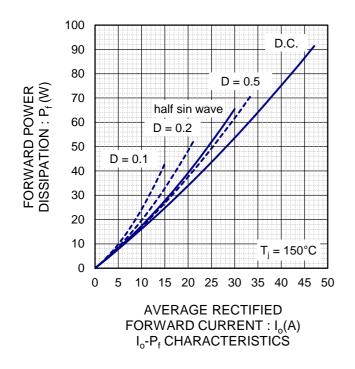
#### •Electrical characteristic curves



# •Electrical characteristic curves



# •Electrical characteristic curves





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