

V_{DSS}	600V
$R_{DS(on)}(Max.)$	3.4Ω
I_D	±1.7A
P_D	20W

●Features

- 1) Low on-resistance.
- 2) Fast switching speed.
- 3) Gate-source voltage (V_{GSS}) guaranteed to be ±20V.
- 4) Drive circuits can be simple.
- 5) Parallel use is easy.
- 6) Pb-free lead plating ; RoHS compliant

●Application

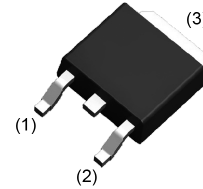
Switching Power Supply

●Absolute maximum ratings ($T_a = 25^{\circ}C$)

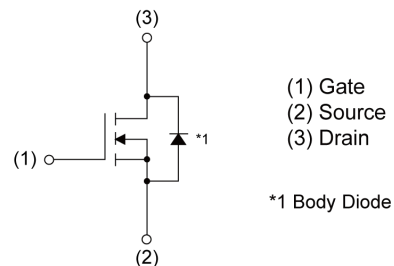
Parameter		Symbol	Value	Unit
Drain - Source voltage		V_{DSS}	600	V
Continuous drain current	$T_C = 25^{\circ}C$	I_D^{*1}	±1.7	A
	$T_C = 100^{\circ}C$	I_D^{*1}	±0.9	A
Pulsed drain current		$I_{D,pulse}^{*2}$	±4	A
Gate - Source voltage		V_{GSS}	±20	V
Avalanche energy, single pulse		E_{AS}^{*3}	6	mJ
Avalanche energy, repetitive		E_{AR}^{*3}	0.04	mJ
Avalanche current		I_{AR}	0.3	A
Power dissipation ($T_C = 25^{\circ}C$)		P_D	20	W
Junction temperature		T_j	150	°C
Range of storage temperature		T_{stg}	-55 to +150	°C
Reverse diode dv/dt		dv/dt ^{*4}	15	V/ns

●Outline

CPT3



●Inner circuit



●Packaging specifications

Type	Packing	Embossed Tape
	Reel size (mm)	330
	Tape width (mm)	16
	Basic ordering unit (pcs)	2500
	Taping code	TL
	Marking	R6002E

● Absolute maximum ratings

Parameter	Symbol	Conditions	Values	Unit
Drain - Source voltage slope	dv/dt	$V_{DS} = 480V$ $T_j = 25^\circ C$	50	V/ns

● Thermal resistance

Parameter	Symbol	Values			Unit
		Min.	Typ.	Max.	
Thermal resistance, junction - case	R_{thJC}	-	-	6.25	$^\circ C/W$
Thermal resistance, junction - ambient	R_{thJA}^{*5}	-	-	147	$^\circ C/W$
Soldering temperature, wavesoldering for 10s	T_{sld}	-	-	265	$^\circ C$

● Electrical characteristics ($T_a = 25^\circ C$)

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Drain - Source breakdown voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 1mA$	600	-	-	V
Zero gate voltage drain current	I_{DSS}	$V_{DS} = 600V, V_{GS} = 0V$ $T_j = 25^\circ C$	-	0.1	100	μA
		$T_j = 125^\circ C$	-	-	1000	
Gate - Source leakage current	I_{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$	-	-	± 100	nA
Gate threshold voltage	$V_{GS(th)}$	$V_{DS} = 10V, I_D = 1mA$	2	-	4	V
Static drain - source on - state resistance	$R_{DS(on)}^{*6}$	$V_{GS} = 10V, I_D = 0.5A$ $T_j = 25^\circ C$	-	2.8	3.4	Ω
		$T_j = 125^\circ C$	-	6.0	-	
Gate input resistance	R_G	$f = 1MHz, \text{open drain}$	-	15.0	-	Ω

●Electrical characteristics ($T_a = 25^\circ\text{C}$)

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Transconductance	g_{fs}^{*6}	$V_{DS} = 10\text{V}, I_D = 0.85\text{A}$	0.5	1.0	-	S
Input capacitance	C_{iss}	$V_{GS} = 0\text{V}$	-	65	-	pF
Output capacitance	C_{oss}	$V_{DS} = 25\text{V}$	-	100	-	
Reverse transfer capacitance	C_{rss}	$f = 1\text{MHz}$	-	12	-	
Effective output capacitance, energy related	$C_{o(er)}$	$V_{GS} = 0\text{V},$ $V_{DS} = 0\text{V to } 480\text{V}$	-	6.4	-	pF
Effective output capacitance, time related	$C_{o(tr)}$		-	21	-	
Turn - on delay time	$t_{d(on)}^{*6}$	$V_{DD} \approx 300\text{V}, V_{GS} = 10\text{V}$	-	12	-	ns
Rise time	t_r^{*6}	$I_D = 0.85\text{A}$	-	16	-	
Turn - off delay time	$t_{d(off)}^{*6}$	$R_L = 357\Omega$	-	25	-	
Fall time	t_f^{*6}	$R_G = 10\Omega$	-	60	-	

●Gate charge characteristics ($T_a = 25^\circ\text{C}$)

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Total gate charge	Q_g^{*6}	$V_{DD} \approx 300\text{V}$	-	6.5	-	nC
Gate - Source charge	Q_{gs}^{*6}	$I_D = 1.7\text{A}$	-	1.7	-	
Gate - Drain charge	Q_{gd}^{*6}	$V_{GS} = 10\text{V}$	-	3.0	-	
Gate plateau voltage	$V_{(plateau)}$	$V_{DD} \approx 300\text{V}, I_D = 1.7\text{A}$	-	6.0	-	V

*1 Limited only by maximum temperature allowed.

*2 $P_w \leq 10\mu\text{s}$, Duty cycle $\leq 1\%$

*3 $I_D = 0.3\text{A}$, $V_{DD} = 50\text{V}$

*4 Reference measurement circuits Fig.5-1.

*5 Mounted on a epoxy PCB FR4 (20mm×20mm×0.8mm).

*6 Pulsed

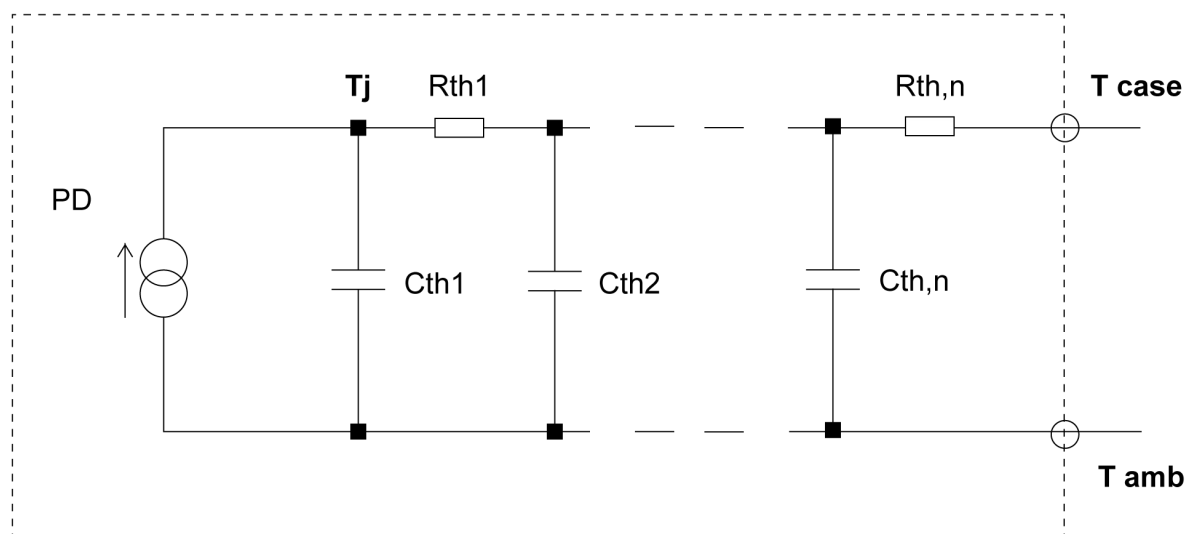
●Body diode electrical characteristics (Source-Drain) ($T_a = 25^\circ\text{C}$)

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Inverse diode continuous, forward current	I_S^{*1}	$T_C = 25^\circ\text{C}$	-	-	1.7	A
Inverse diode direct current, pulsed	I_{SM}^{*2}		-	-	4	A
Forward voltage	V_{SD}^{*6}	$V_{GS} = 0\text{V}, I_S = 1.7\text{A}$	-	-	1.5	V
Reverse recovery time	t_{rr}^{*6}	$I_S = 1.7\text{A}$ $di/dt = 100\text{A}/\mu\text{s}$	-	240	-	ns
Reverse recovery charge	Q_{rr}^{*6}		-	0.86	-	μC
Peak reverse recovery current	I_{rm}^{*6}		-	7.0	-	A

●Typical transient thermal characteristics

Symbol	Value	Unit
R_{th1}	1.95	K/W
R_{th2}	2.54	
R_{th3}	22.4	
R_{th4}	48.3	

Symbol	Value	Unit
C_{th1}	0.000735	Ws/K
C_{th2}	0.00802	
C_{th3}	0.104	
C_{th4}	1.20	



●Electrical characteristic curves

Fig.1 Power Dissipation Derating Curve



Fig.2 Normalized Transient Thermal Resistance vs. Pulse Width

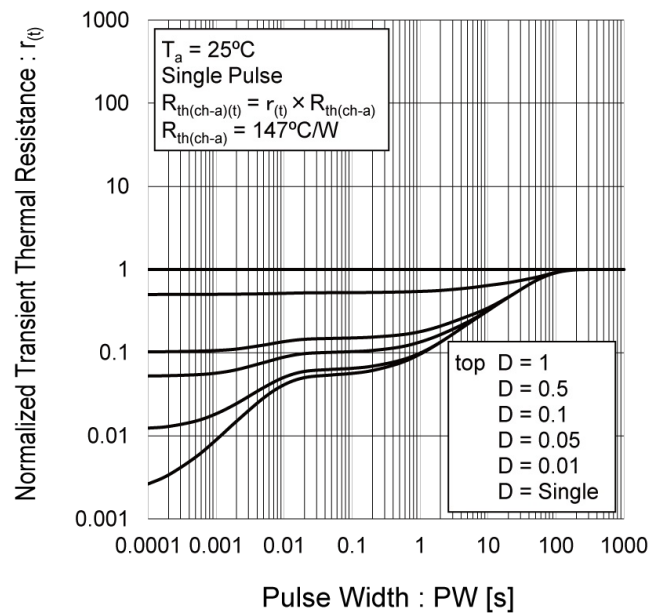
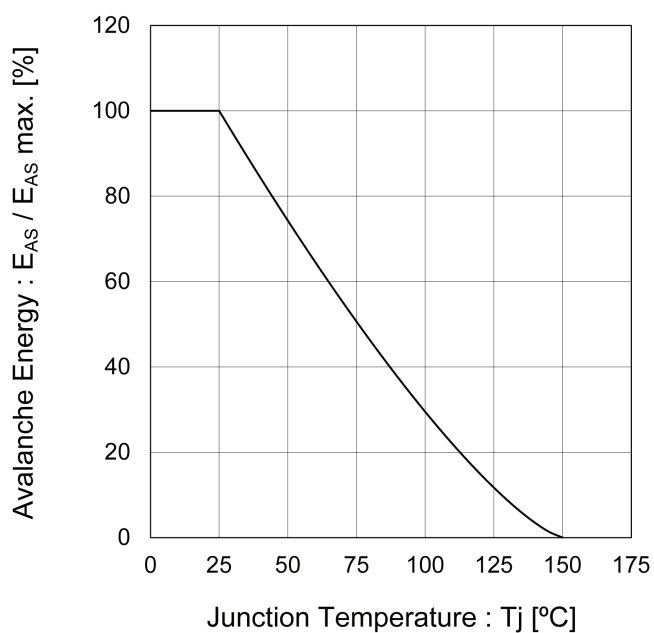


Fig.3 Avalanche Energy Derating Curve vs Junction Temperature



●Electrical characteristic curves

Fig.4 Typical Output Characteristics(I)

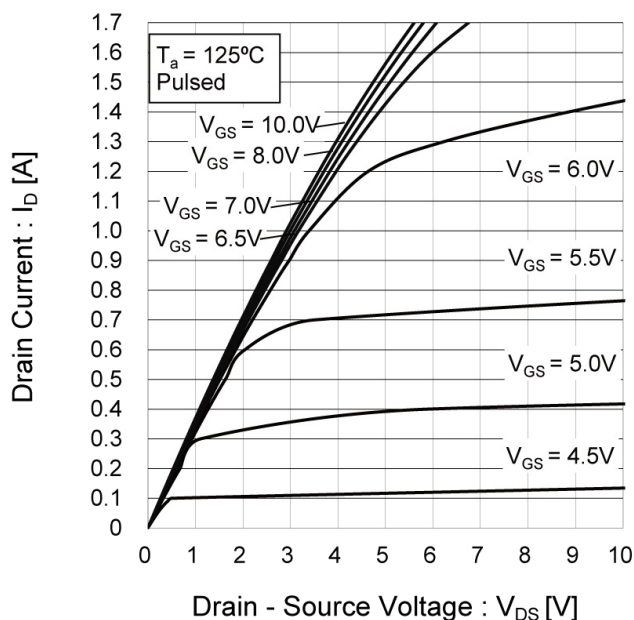
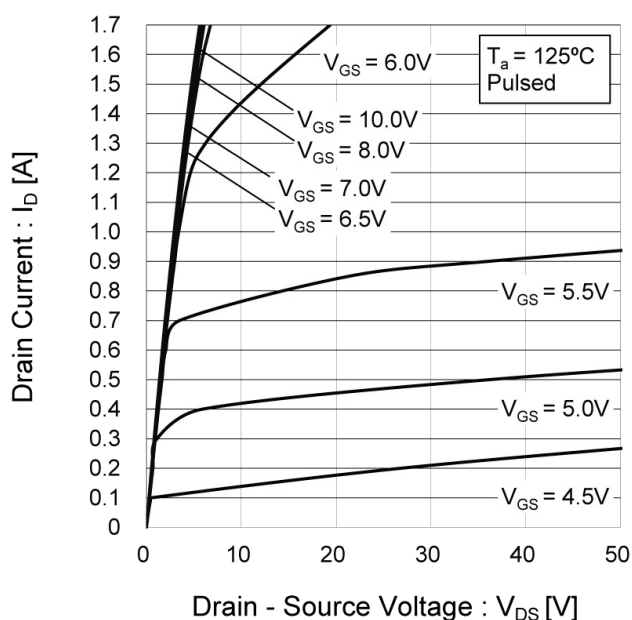
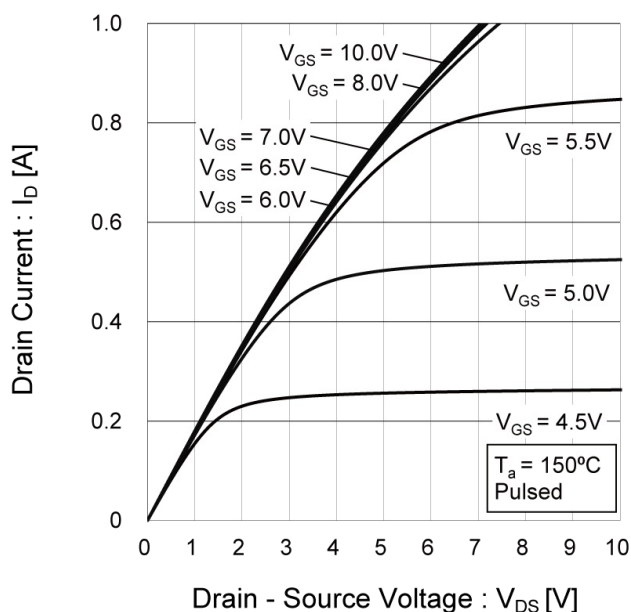
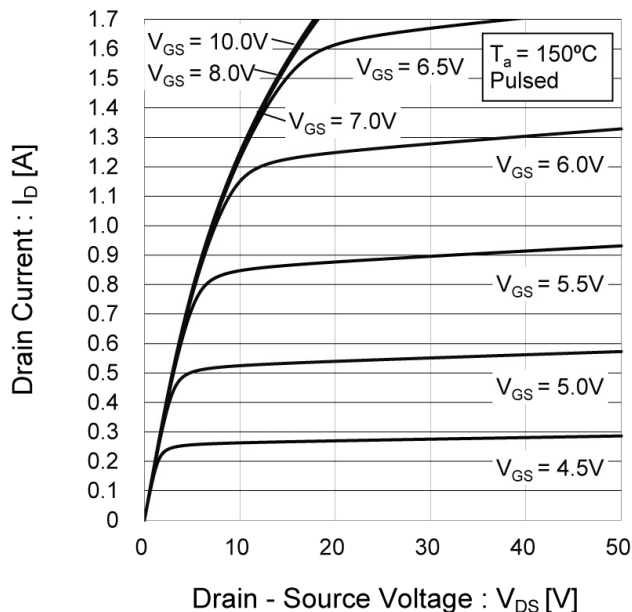


Fig.5 Typical Output Characteristics(II)

Fig.6 $T_j = 150^\circ\text{C}$ Typical Output Characteristics (I)Fig.7 $T_j = 150^\circ\text{C}$ Typical Output Characteristics (II)

●Electrical characteristic curves

Fig.8 Breakdown Voltage vs. Junction Temperature

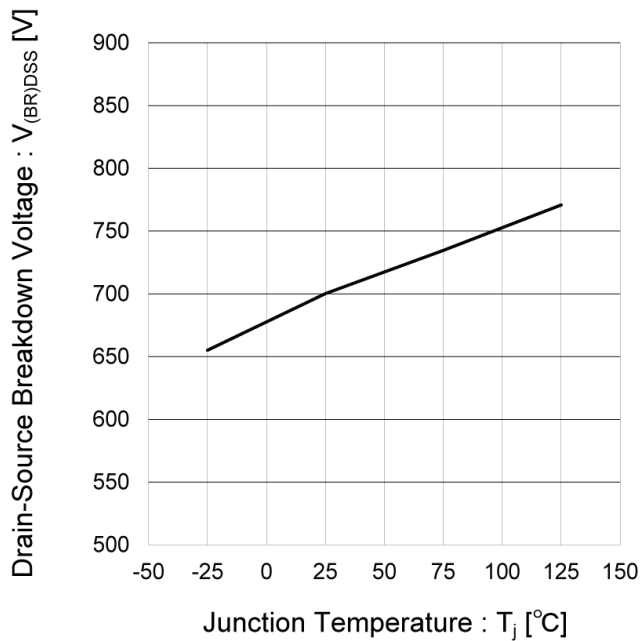


Fig.9 Typical Transfer Characteristics

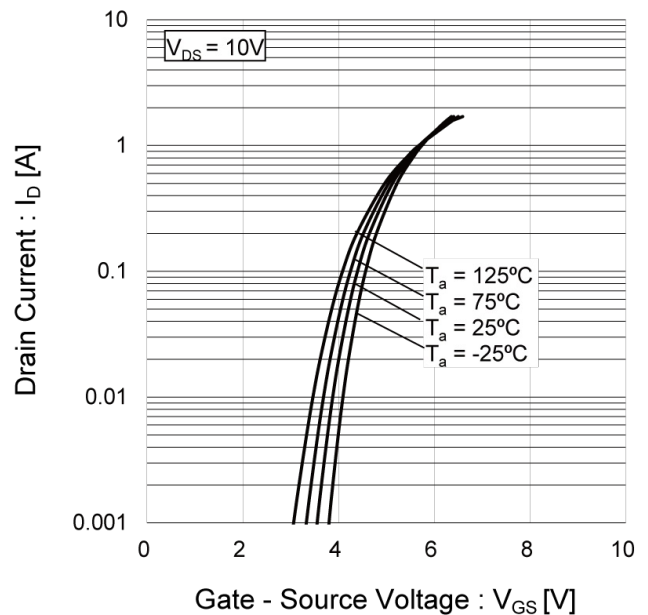


Fig.10 Gate Threshold Voltage vs. Junction Temperature

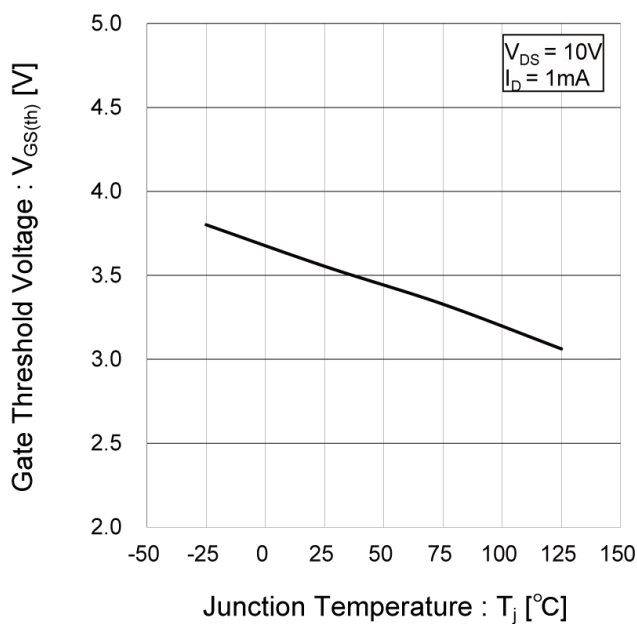
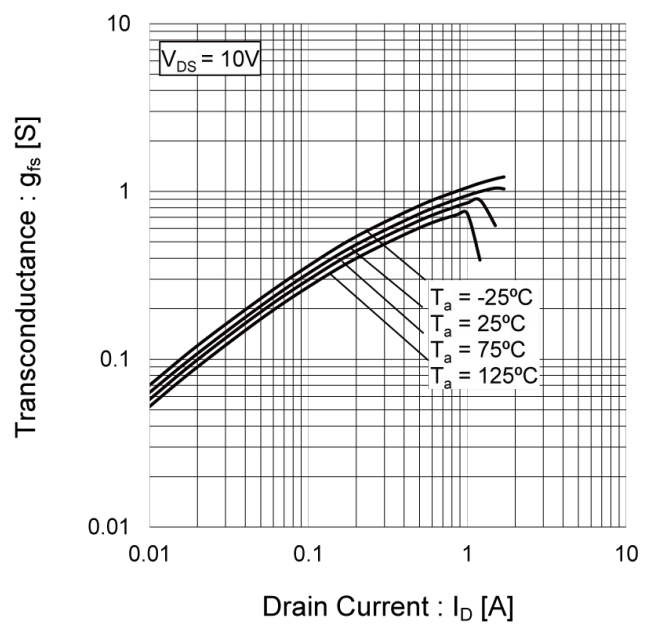


Fig.11 Transconductance vs. Drain Current



●Electrical characteristic curves

Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

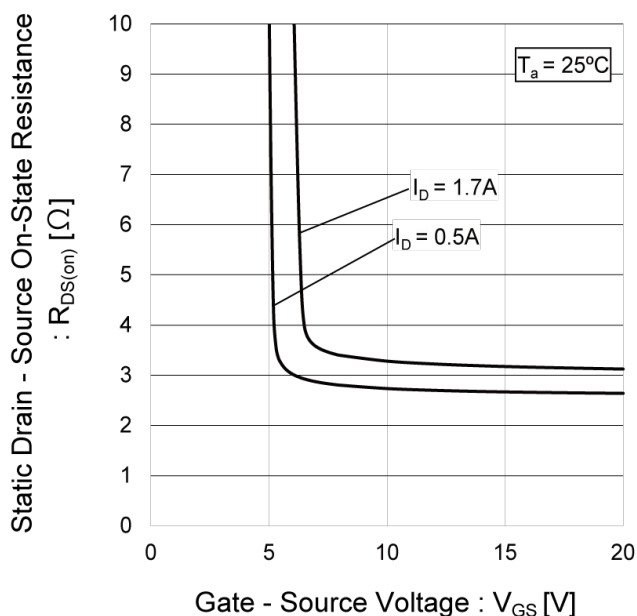


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature

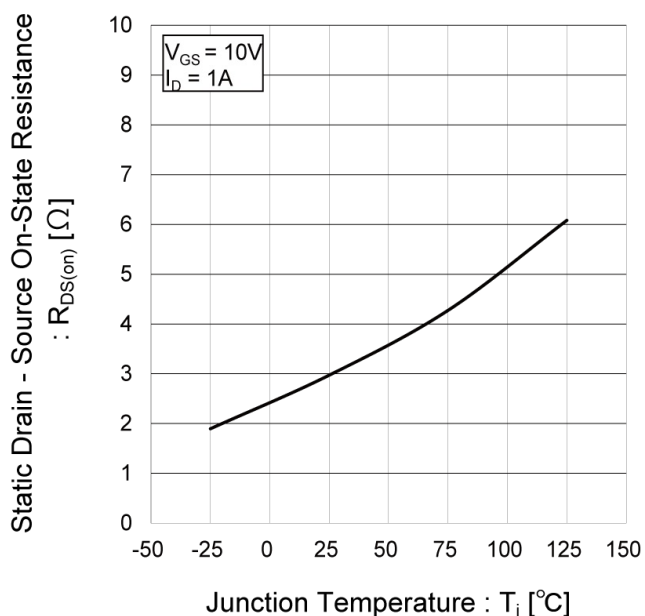


Fig.14 Static Drain - Source On - State Resistance vs. Drain Current(I)

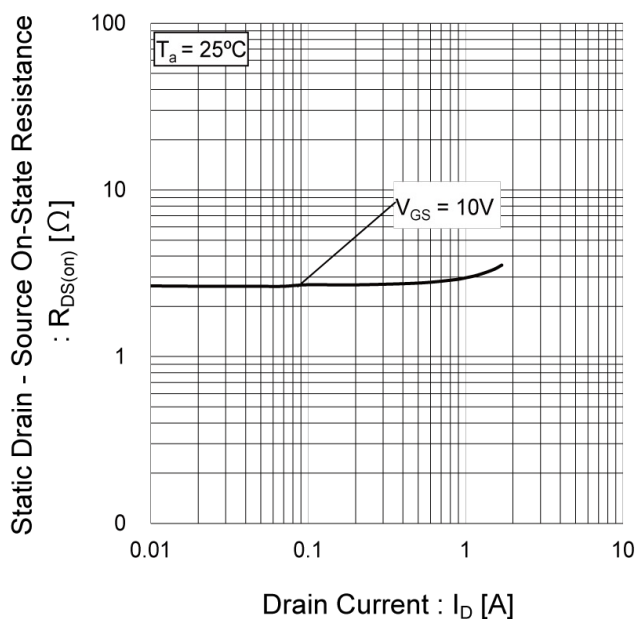
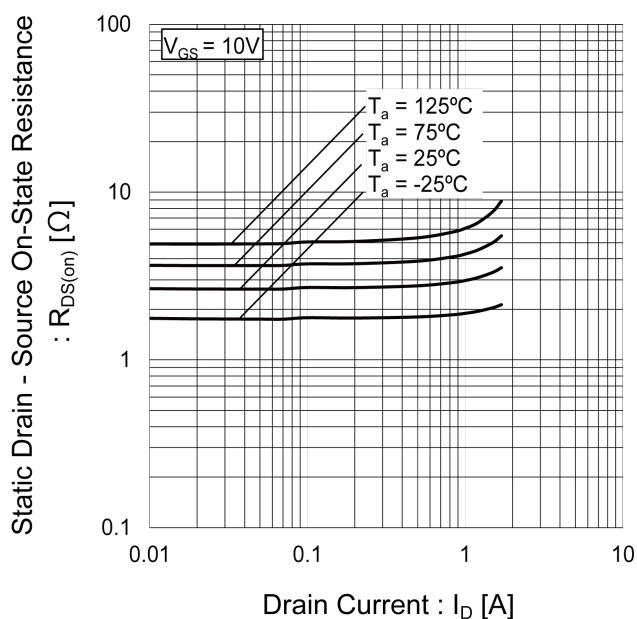


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current(II)



●Electrical characteristic curves

Fig.16 Typical Capacitance vs. Drain - Source Voltage

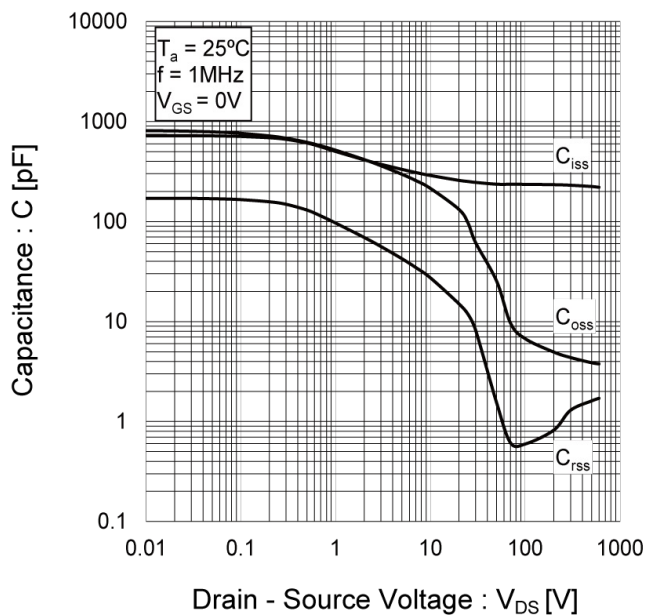


Fig.17 Coss Stored Energy

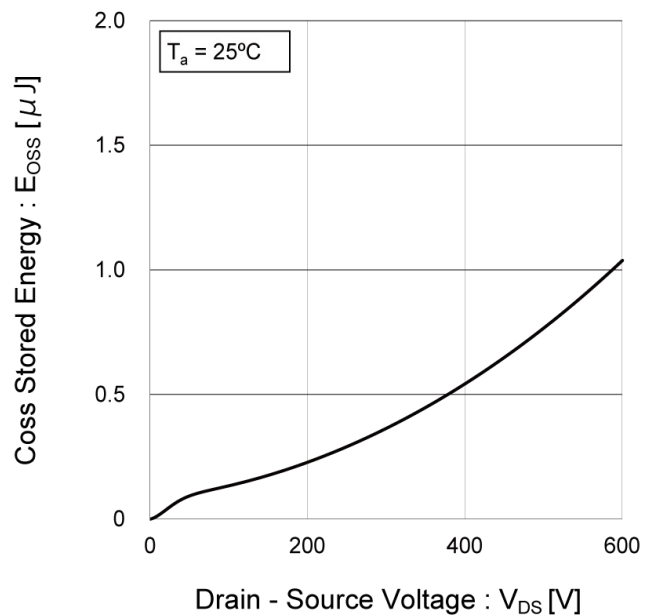


Fig.18 Switching Characteristics

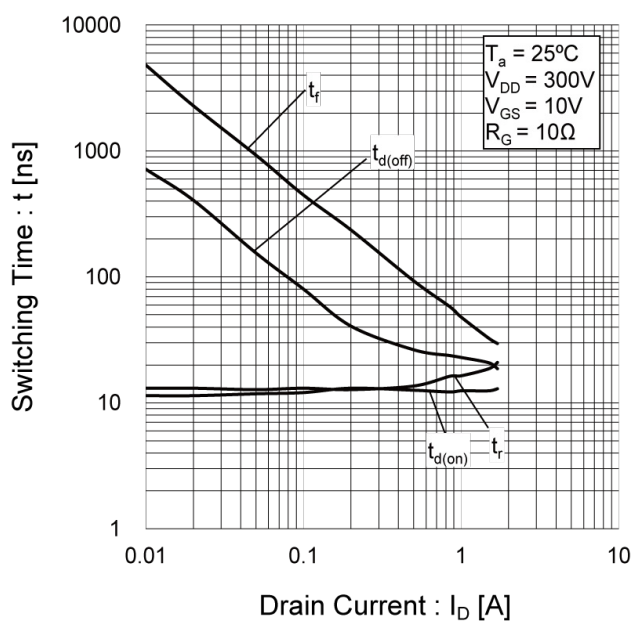
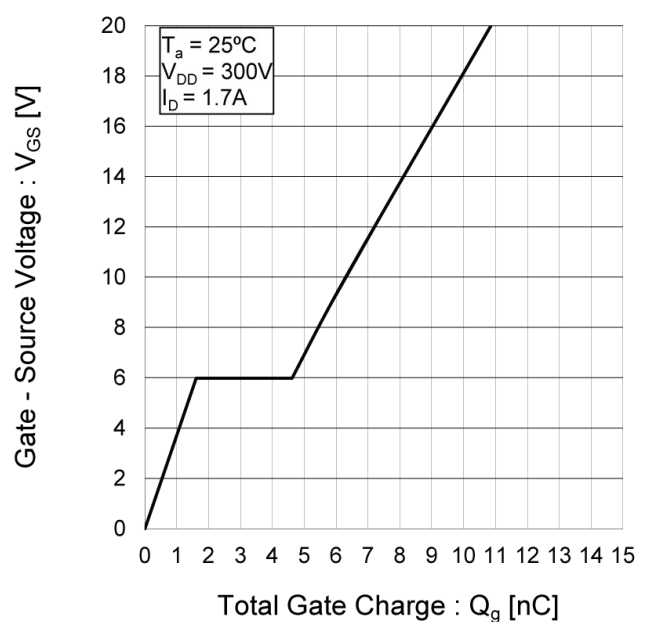


Fig.19 Dynamic Input Characteristics



●Electrical characteristic curves

Fig.20 Inverse Diode Forward Current vs.
Source - Drain Voltage

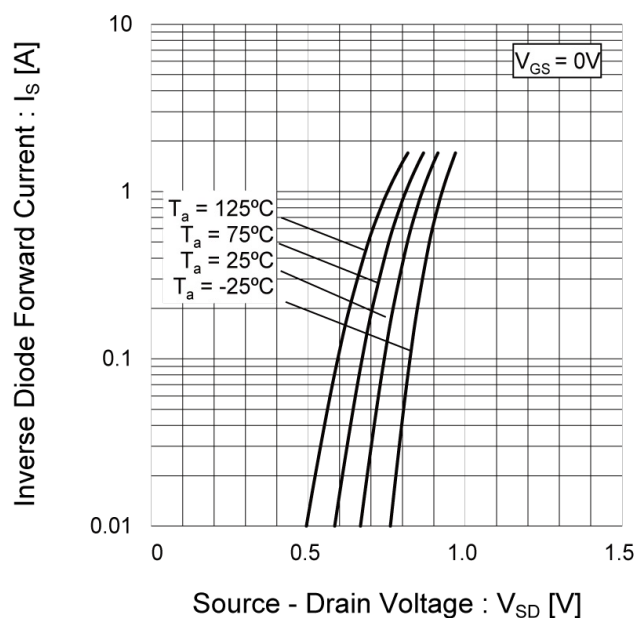
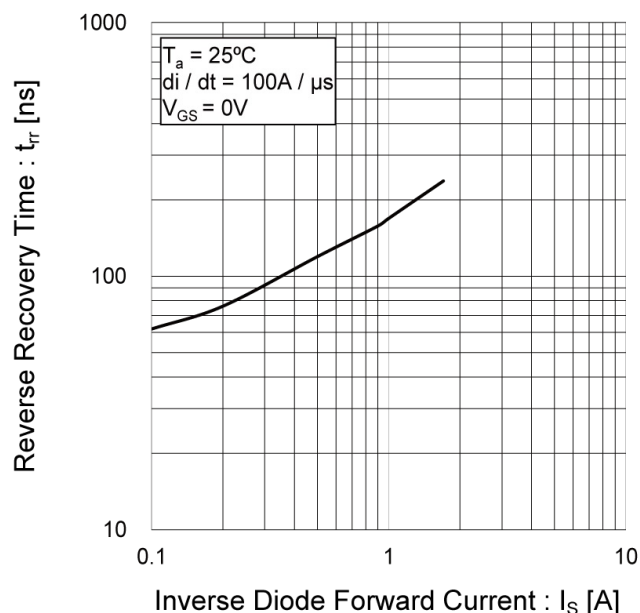


Fig.21 Reverse Currentse Recovery
Time vs. Inverse Diode



● Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

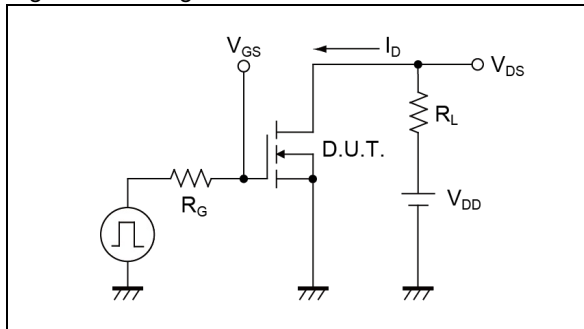


Fig.1-2 Switching Waveforms

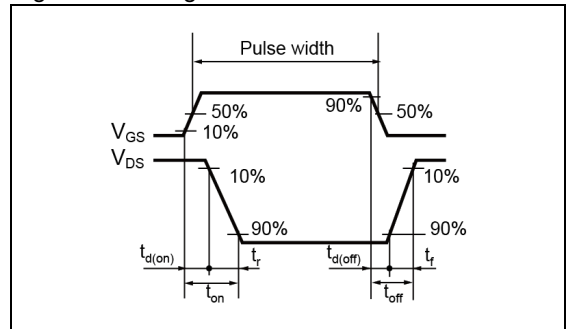


Fig.2-1 Gate Charge Measurement Circuit

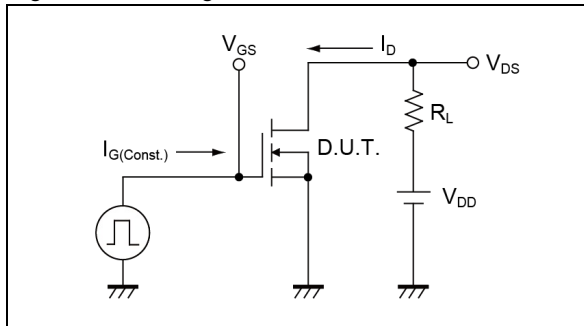


Fig.2-2 Gate Charge Waveform

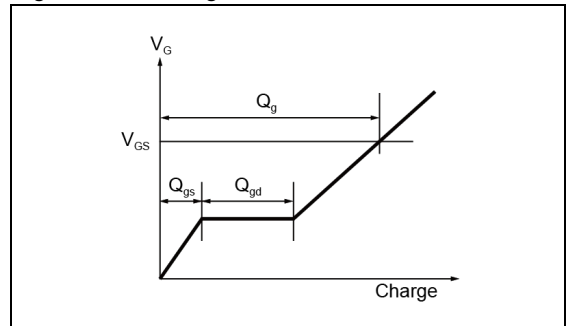


Fig.3-1 Avalanche Measurement Circuit

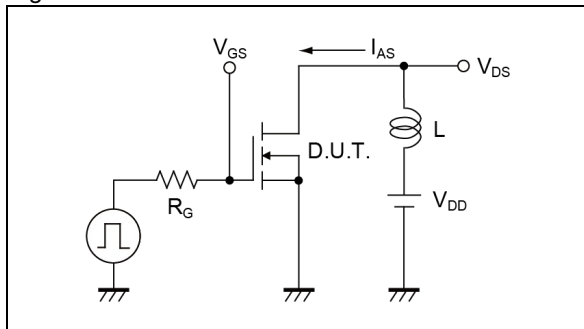


Fig.3-2 Avalanche Waveform

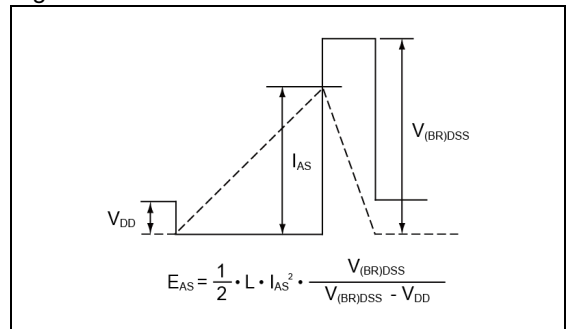


Fig.4-1 dv/dt Measurement Circuit

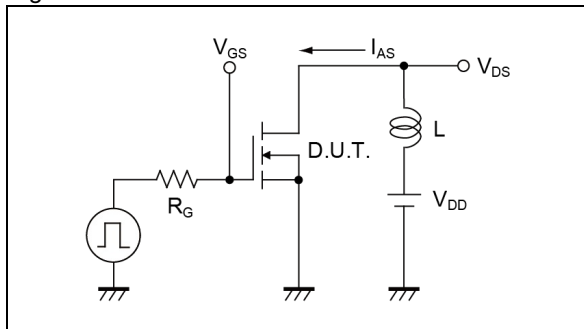


Fig.4-2 dv/dt Waveform

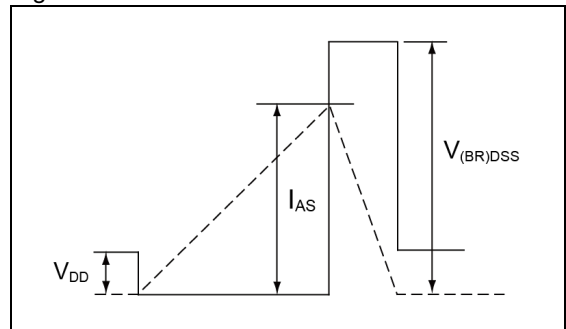


Fig.5-1 di/dt Measurement Circuit

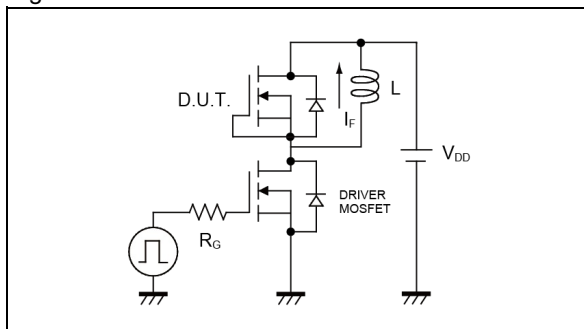
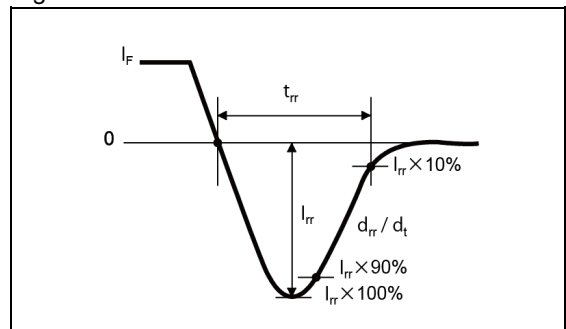
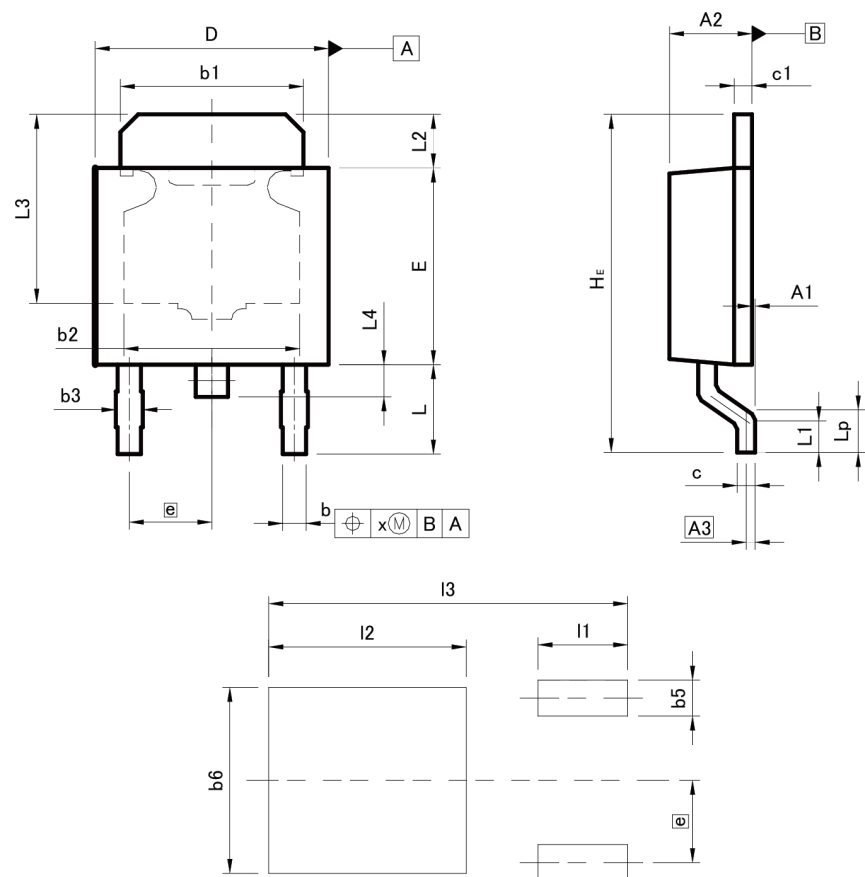


Fig.5-2 di/dt Waveform



●Dimensions

CPT



Pattern of terminal position areas
[Not a recommended pattern of soldering pads]

DIM	MILIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A1	0.00	0.15	0.000	0.006
A2	2.20	2.50	0.087	0.098
A3	0.25		0.010	
b	0.55	0.75	0.022	0.030
b1	5.00	5.30	0.197	0.209
b2	5.00		0.197	
b3	0.75		0.030	
c	0.40	0.60	0.016	0.024
c1	0.40	0.60	0.016	0.024
D	6.30	6.70	0.248	0.264
E	5.40	5.80	0.213	0.228
e	2.30		0.091	
HE	9.00	10.00	0.354	0.394
L	2.20	2.80	0.087	0.110
L1	0.80	1.40	0.031	0.055
L2	1.20	1.80	0.047	0.071
L3	5.30		0.209	
L4	0.90		0.035	
Lp	1.00	1.60	0.039	0.063
x	-	0.25	-	0.010

DIM	MILIMETERS		INCHES	
	MIN	MAX	MIN	MAX
b5	-	1.00	-	0.04
b6	-	5.20	-	0.205
I1	-	2.50	-	0.098
I2	-	5.50	-	0.217
I3	-	10.00	-	0.394

Dimension in mm/inches

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