



Typical Feature

- ◆ Fixed Input Voltage, isolated & unregulated dual Output, power 1W
- ◆ Operating Temperature: -40°C to +105°C
- ◆ Small SMD package, international standard pin out
- ◆ Isolation Voltage 1500VDC
- ◆ High efficiency up to 86%
- ◆ Low no load input current



Application Filed

NN1-XXDXXANT is suitable for pure digital systems, low frequency analog circuits, relay-driven circuits. It is specially designed for applications where an isolated voltage is required in a distributed power supply system.

It could be widely used in the below products:

1. *The voltage of the input power supply is relatively stable(voltage change range:±10%Vin)*
2. *Isolation between input and output is required (Isolation Voltage≤1500VDC);*
3. *Low requirements for output voltage stability and output ripple noise;*

Typical Product List

Part No	Input Voltage	Output Voltage/Current		Input Current(mA)		Max. Capacitive Load	Ripple & Noise 20MHz (TYP/MAX)	Efficiency (MIN/TYP)
	(VDC)	Voltage	Current	Nominal Voltage				
	Range	(VDC)	(mA) MAX / MIN	Full load typ.	No load typ.			
NN1-05D05ANT	5(4.5-5.5)	±5	±100/±10	230	8	1200	80/100	81/84
NN1-05D12ANT		±12	±42/±4	226	14	470	80/100	81/84
NN1-12D12ANT	12(10.8-13.2)	±12	±42/±4	96	8	470	80/100	82/85
NN1-12D15ANT		±15	±33/±3	92	9	470	80/100	83/86

Note 1: The typical output efficiency is based on that product is full loaded and burned-in after half an hour.

Note 2: The fluctuation range of full load efficiency(% ,TYP) is ±3%, full load output efficiency= total output power/module's input power.

Note 3: Ripple & Noise Tested by twisted-pair method, for details please check Ripple& Noise Test Method.

Input Specifications

Item	Operating Condition	Min.	Typ.	Max.	Unit
Reflected Ripple Current	-	-	15	-	mA
Overshoot Voltage	5Vdc Input	-0.7	-	9	VDC
	12Vdc Input	-0.7	-	18	
Overshoot Current	-	-	0.8	-	A
Input Filter Type	Capacitor Filter				
Hot Plug	Unavailable				

Output Specifications



Item	Operating Condition		Min.	Typ.	Max.	Unit
Output Voltage Accuracy	-		See Regulation Curve			
Line Regulation	Input voltage change ±1%	3.3Vdc/5Vdc Output	-	-	±2.0	%
		Other Output	-	-	±1.5	
Load Regulation	10%-100% load	3.3Vdc/5Vdc Output	-	10	15	%
		Other Output	-	8	10	
Temperature Drift Coefficient	Full load		-	-	±0.03	%/°C
Short Circuit Protection	Continuous, Self-recovery					

General Specifications

Item	Operating Condition	Min.	Typ.	Max.	Unit
Insulation Withstand Voltage	Input-output, Test 1min, leakage current≤0.5mA	1500	-	-	VDC
Insulation Resistance	Input-output, Insulation Voltage 500VDC	1000	-	-	MΩ
Isolation Capacitor	Input-output, 100KHz/0.1V	-	20	-	PF
Operating Temperature	Temperature≥105°C, see Temperature Derating Curve	-40	-	85	°C
Case Temperature Rise	Ambient Temperature 25°C	-	15	25	
Storage Temperature	-	-55	-	135	
Reflow Temperature	Peak temperature Tc≤245°C, for above 217°C max 60S				
Storage Humidity	No condensing	-	-	95	%RH
Switching Frequency	Full load, Input Standard Voltage	-	330	-	KHz
MTBF	MIL-HDBK-217F@25°C	3000			K hours

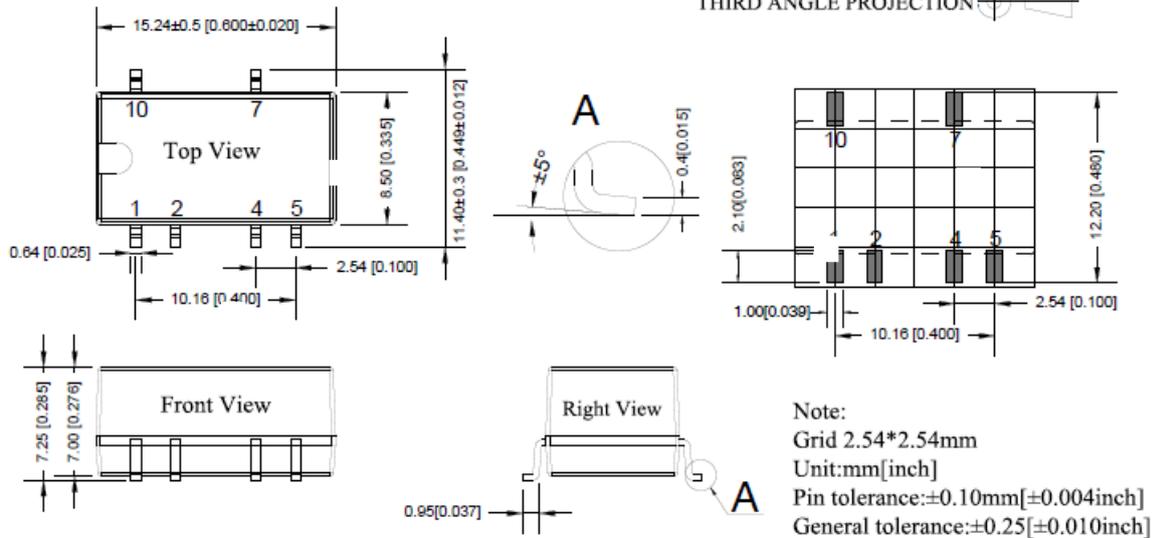
Material Characteristics

Case Material	Black flame-retardant heat-resistant plastic (UL94 V-0)				
Packing Dimension	SMD package	15.24X11.40X7.25 mm			
Product Weight		1.4g(TYP)			
Cooling Method	Natural air cooling				

EMC Character

EMI	CE	CISPR32/EN55032 CLASS B (See EMC recommended circuit)
	RE	CISPR32/EN55032 CLASS B (See EMC recommended circuit)

Packing Information



Pin Definition

Pin-Out	1	2	4	5	7	10
Function	GND	Vin	0V	-Vo	+Vo	*NC

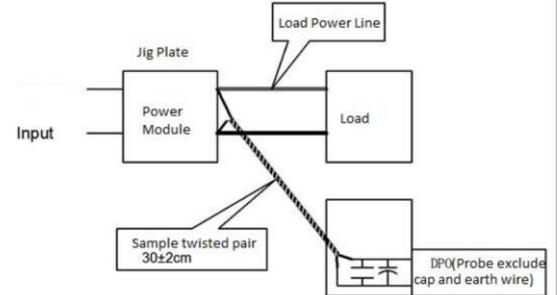
*NC:cannot connected to any external circuit; pin specs:0.25*0.64; unit:mm

Ripple & Noise Test(Twisted Pair Method 20MHz bandwidth)

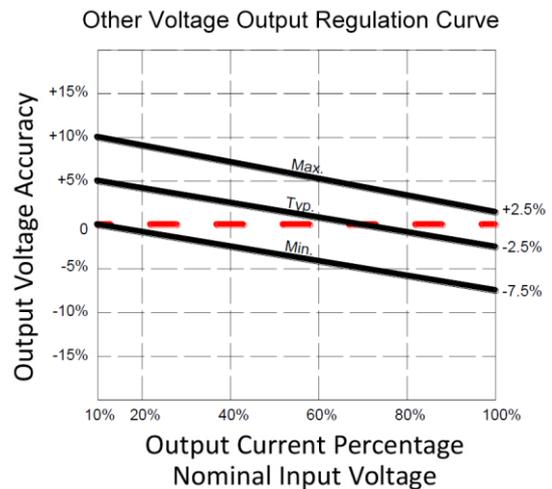
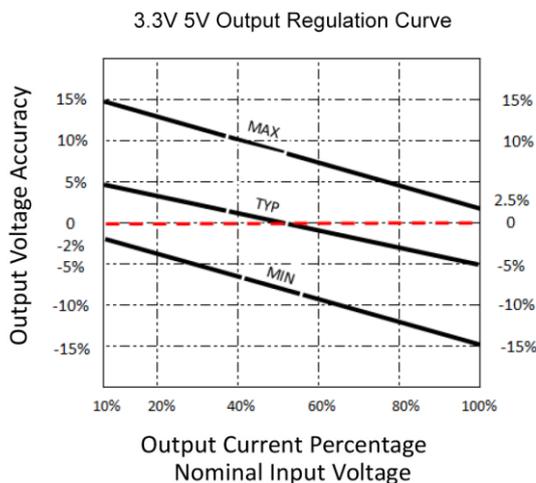
Test Method:

(1) 12# twisted pair to connect, Oscilloscope bandwidth set as 20MHz, 100M bandwidth probe, terminated with 0.1uF polypropylene capacitor and 4.7uF high frequency low resistance electrolytic capacitor in parallel, oscilloscope set as Sample pattern.

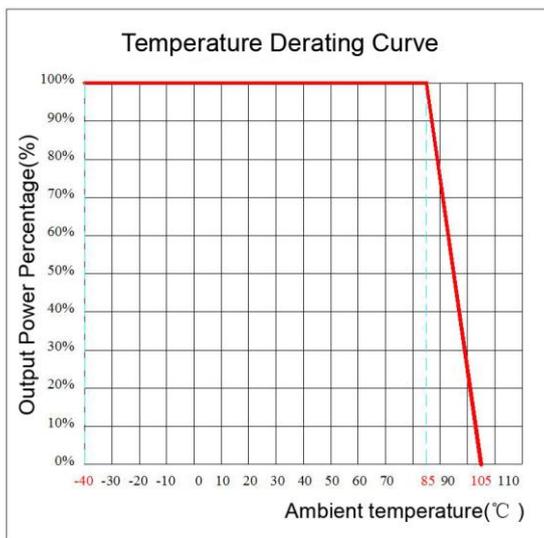
(2) Input terminal connect to power supply, output terminal connect to electronic load through jig plate, Use 30cm±2 cm sampling line, Power line selected from corresponding diameter wire with insulation according to the flow of output current.



Output Voltage Regulation Curve



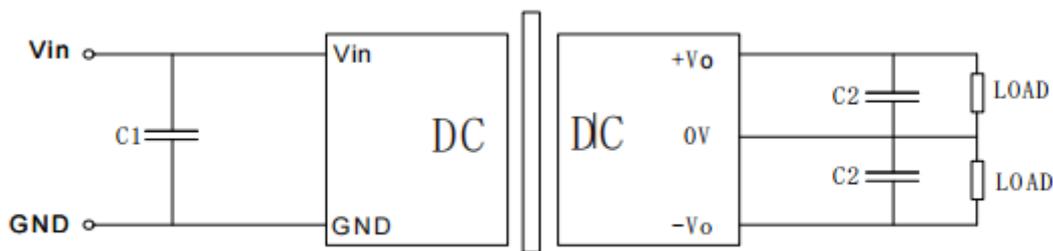
Products Characteristic Curve



Application Circuit

1. Typical Application

In order to ensure the input/output ripple and noise decreased, capacitor filter net could be connected to input and output side, application circuit as below photo 3; choosing suitable filter capacitor is very important, start-up problems may be caused by too large capacitance.



Note 1: C_{in} is 4.7uF/50V, C_{out} is 10uF/50V

2. EMC Typical Recommended Circuit



Note 2: C1 is 4.7uF/50V, LDM is 6.8uH, Cout is 10uF/50V

3. Output Load Requirements

In order to ensure that the module can work efficiently and reliably, its output minimum load cannot be less than 10% of the rated load when in use. If the power you need is really small, please connect a resistors in parallel (the sum of the power consumed by the resistor and the actual power used is greater than or equal to 10% of the rated power).

Note:

1. If the product is operated under the min. required load, the product performance cannot be guaranteed to comply with all performance indexes in this datasheet;
2. The maximum capacitive load is tested under nominal input voltage range and full load condition;
3. Unless otherwise specified, data in this datasheet are tested under conditions of **Ta=25°C**, **humidity<75%** when inputting nominal voltage and outputting rated load(pure resistance load);