



PIN DESCRIPTION :KX023

No.	Symbol	Function
1	IO Vdd	The power supply input for the digital communication bus.
2,3	NC	Not Internally Connected
4	SCLK/SCL	SPI and I ² C Serial Clock
5	GND	Ground
6	SDI/SDA	SPI Data input/I2C Serial Data
7	SDO/ADDR	Serial Data Out pin during 4 wire SPI communication
8	nCS	SPI enable/I2C mode select
9	INT2	Physical Interrupt 2
10,15,16	NC	Not Internally Connected
11	INT1	Physical Interrupt 1
12	GND	Ground
13	TRIG	Trigger pin for FIFO buffer control-Connect to GND when not using external trigger option
14	Vdd	The power supply input.

PHYSICAL BOARD DIMENSIONS

